

MEMORY MANAGEMENT SYSTEM HAVING A LINKED LIST PROCESSOR**ABSTRACT**

A memory management system adapted to process linked list data files. The
5 system has a plurality of low storage capacity high speed memories and a lower speed
high storage capacity bulk memory. An access flow regulator generates requests for
the reading and writing of linked list files by the memories. The head and tail buffers
and at any intermediate buffers of a linked list are written into the high speed memories.
The intermediate buffers are immediately transferred from the high speed memories to
10 said bulk memory while leaving the head buffer and the tail buffer of the linked list in the
high speed memories. In read operations, the head and tail buffers are read from the
high speed memories. The intermediate buffers are transferred from the bulk memory
to said the high speed memory and then read from the high speed memories.